

FIG. 1

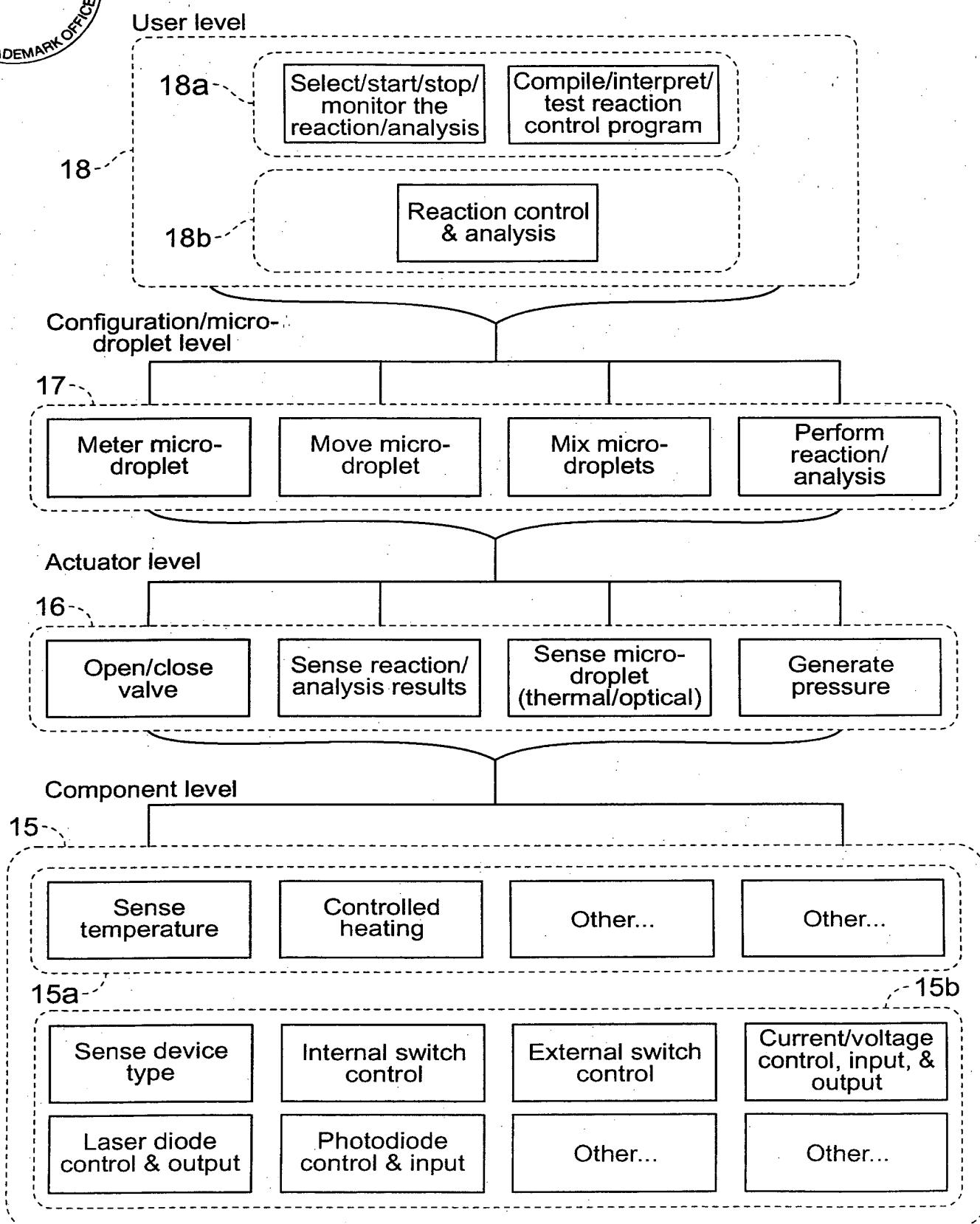


FIG.2

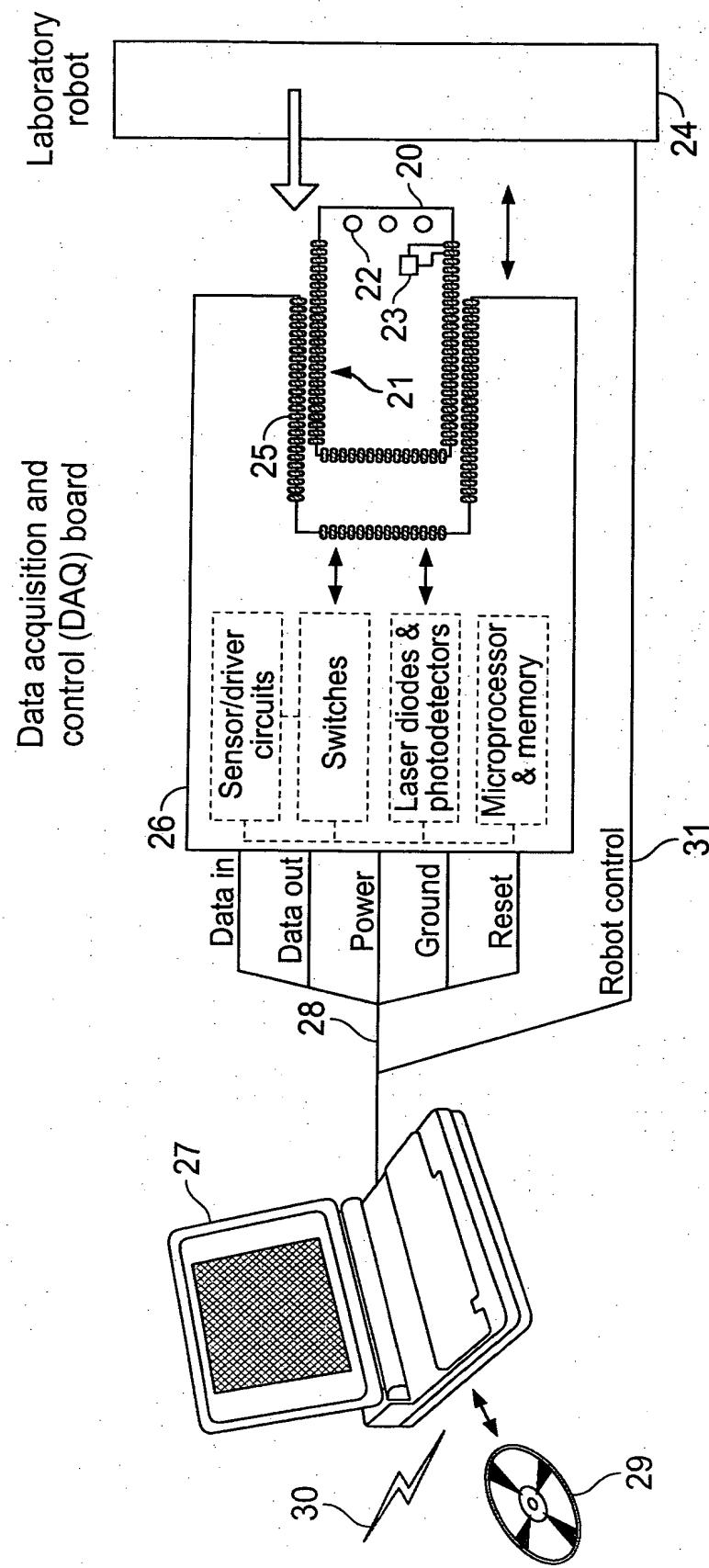


FIG. 3A

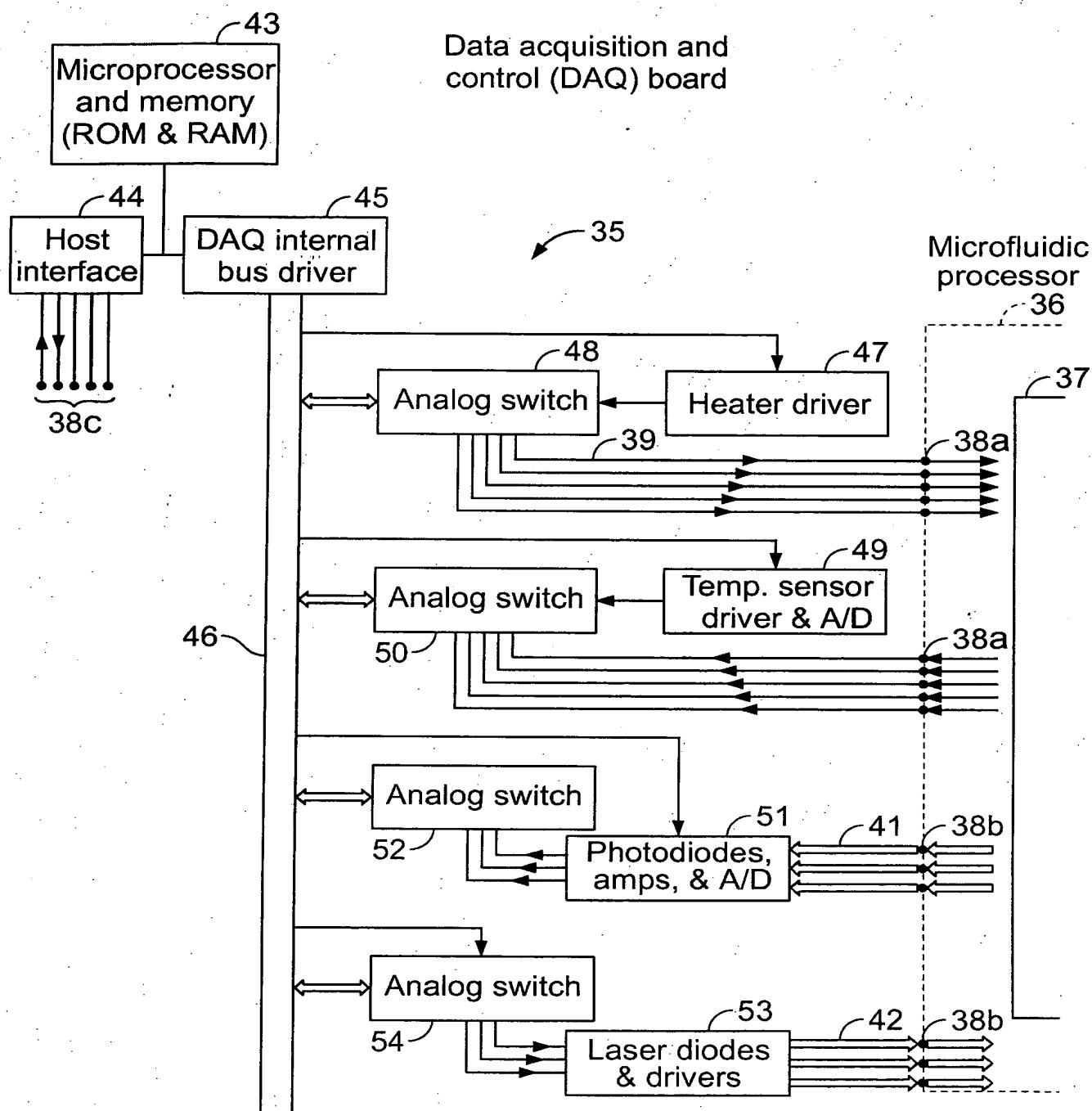


FIG. 3B

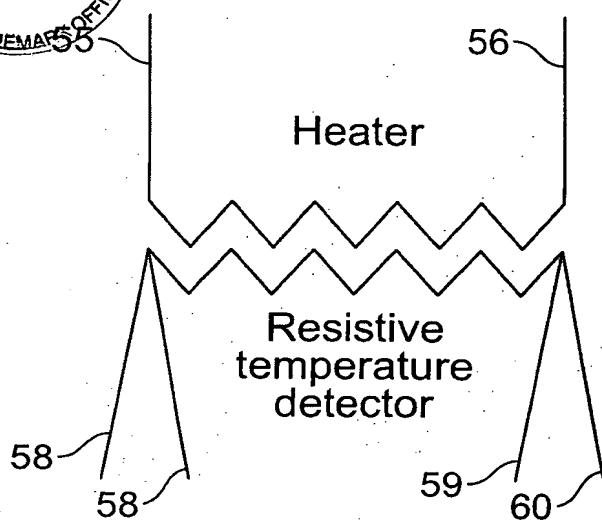


FIG. 4A

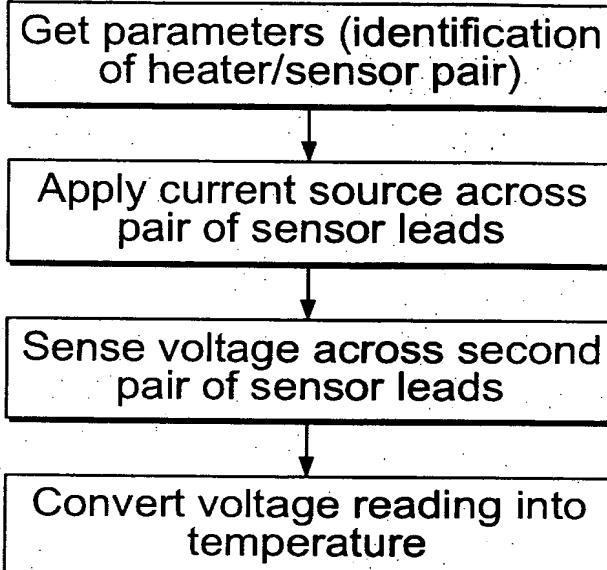


FIG. 4B

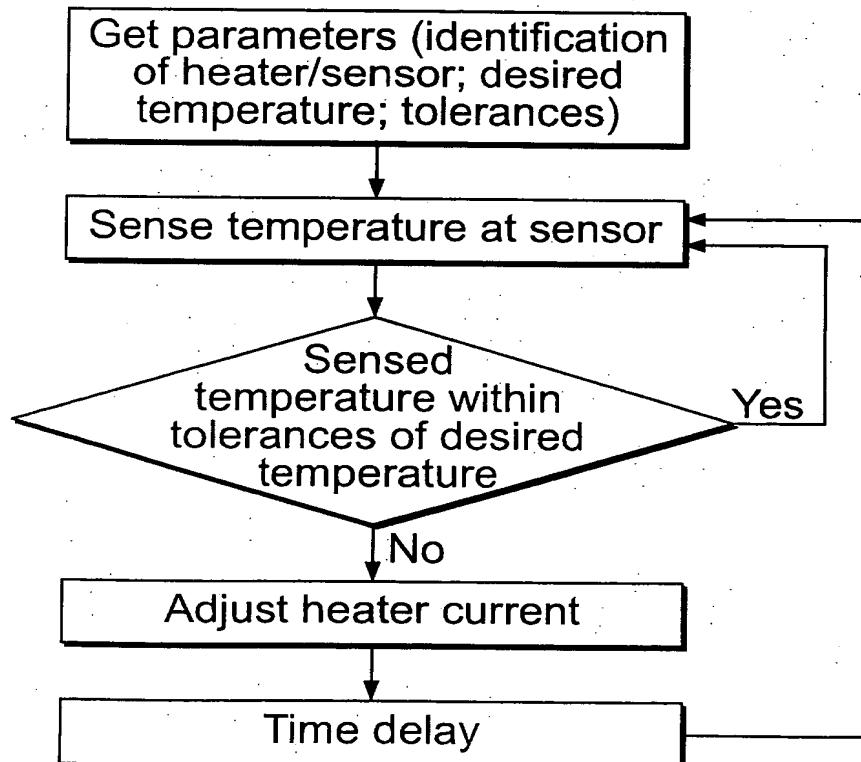


FIG. 4C

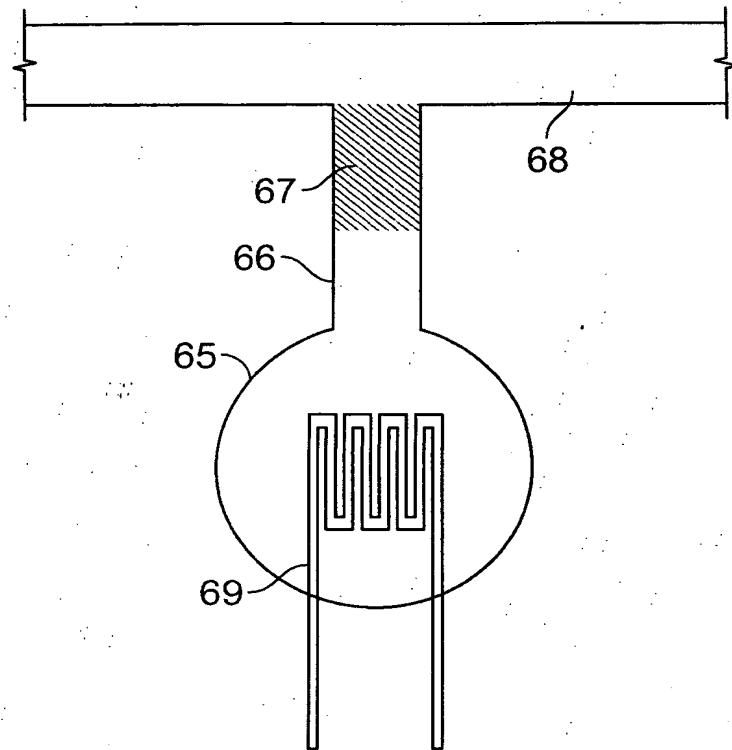


FIG. 5A

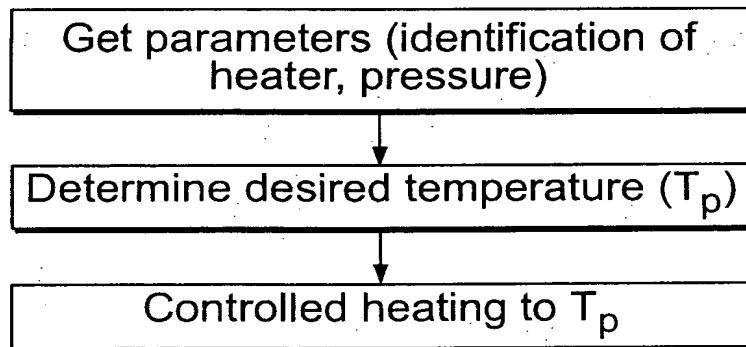


FIG. 5B

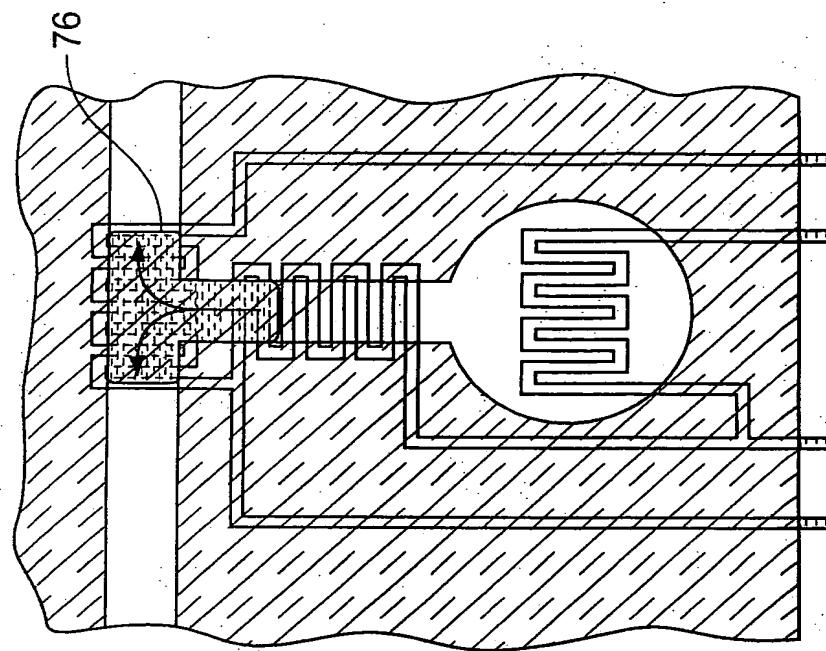


FIG. 6B

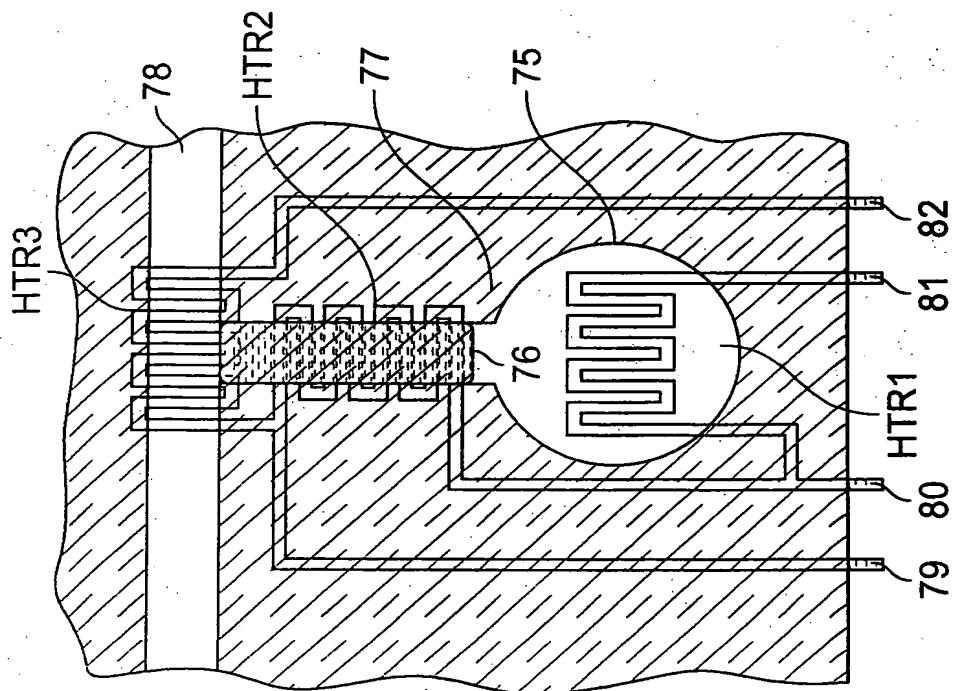


FIG. 6A

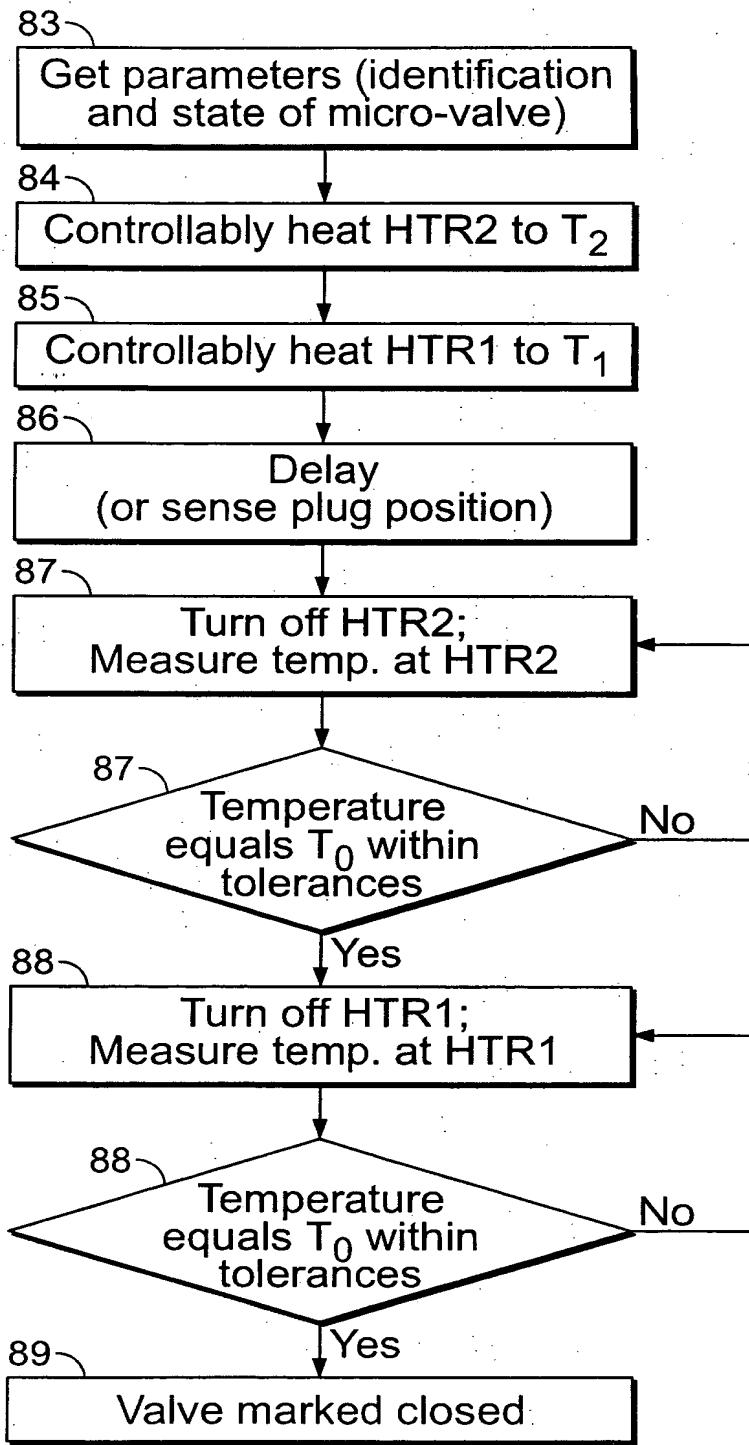


FIG. 6C

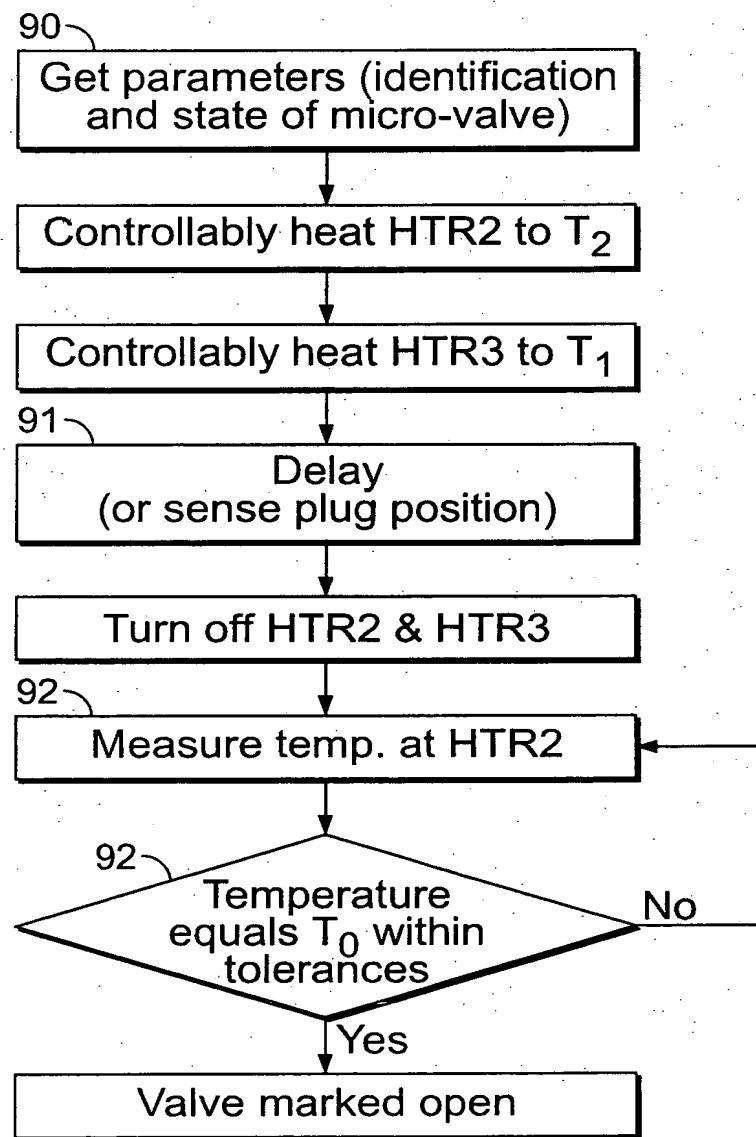


FIG. 6D

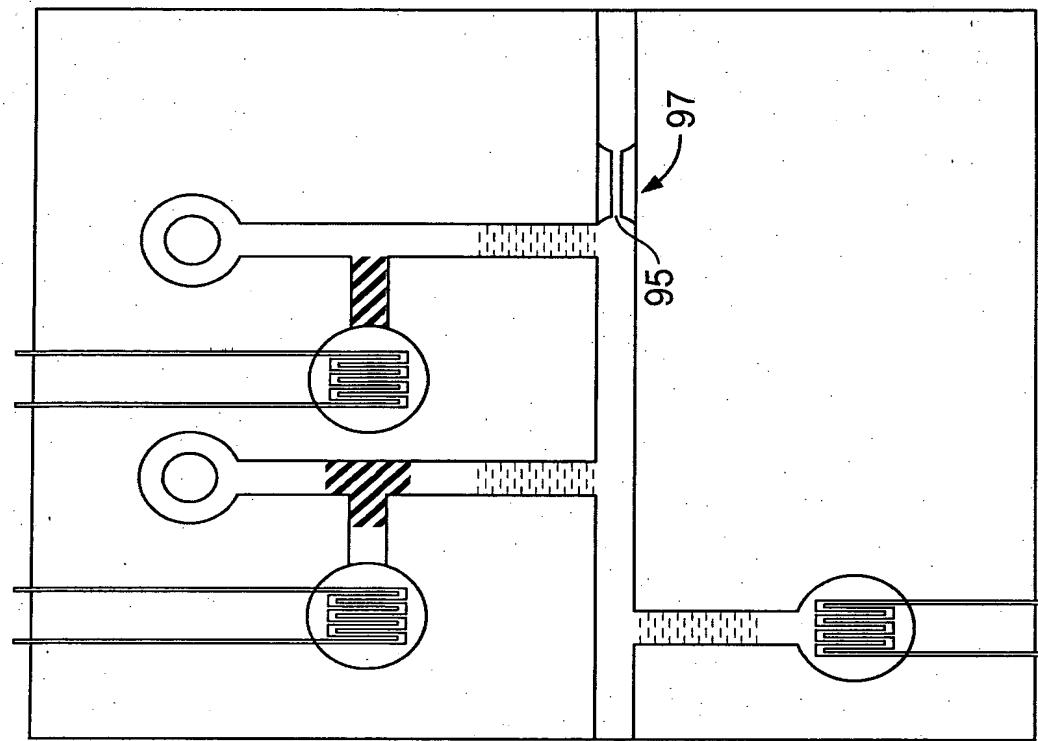


FIG. 7B

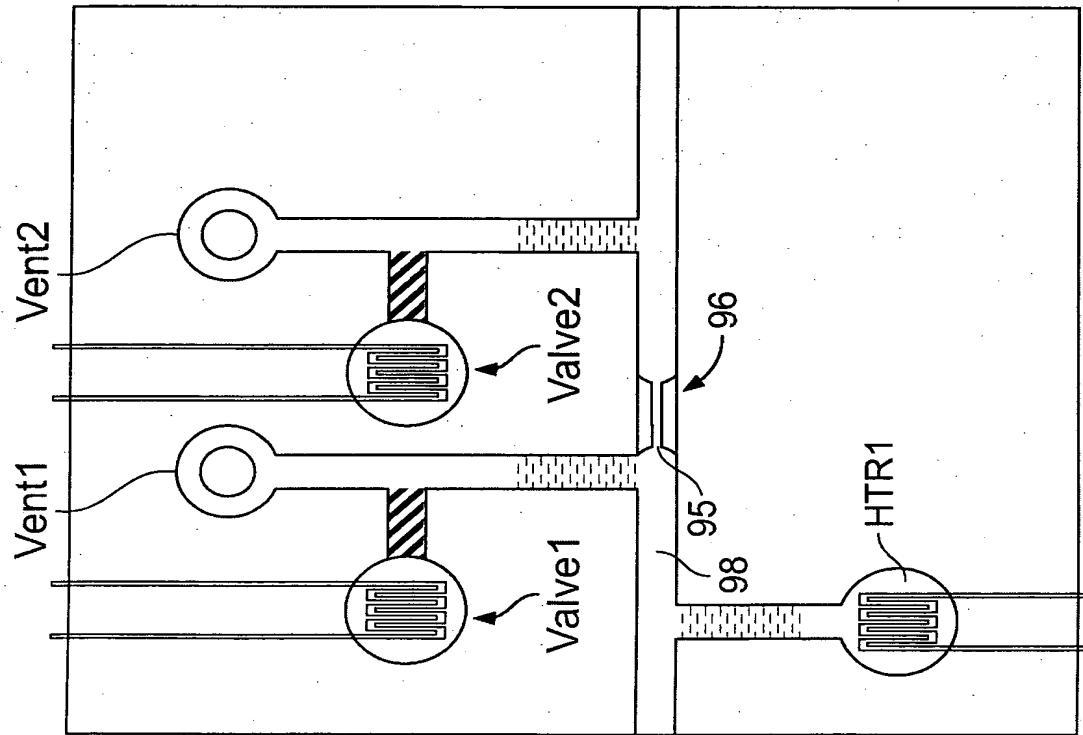


FIG. 7A

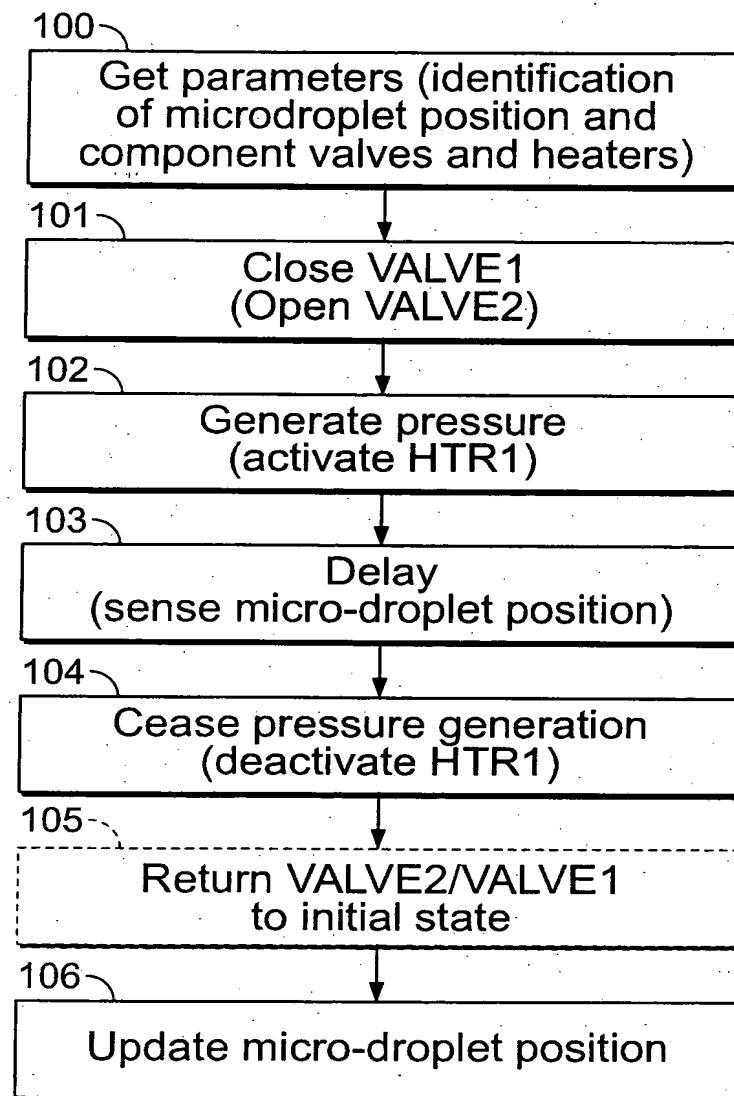
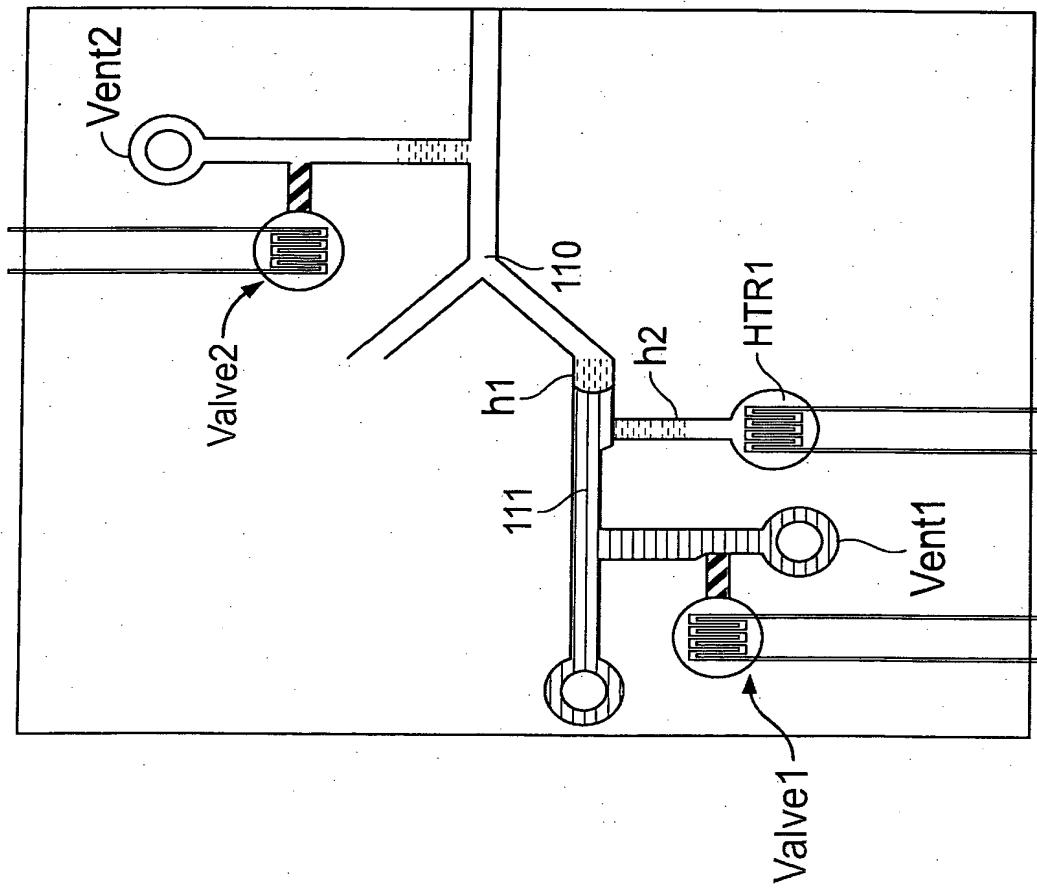
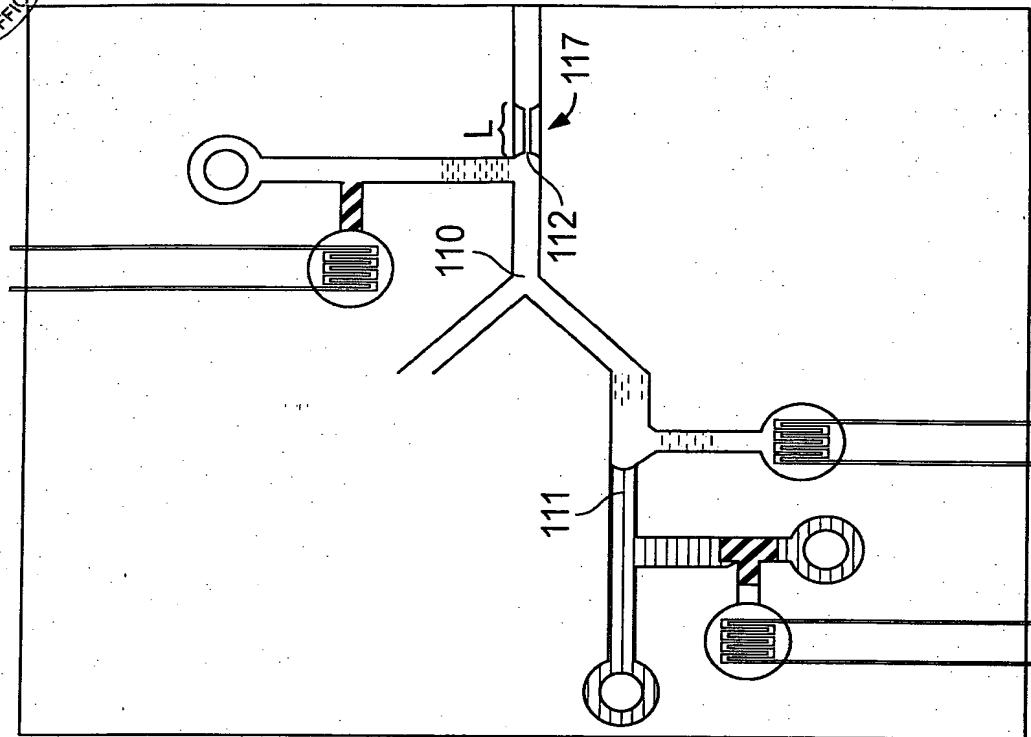


FIG. 7C





Port1 →

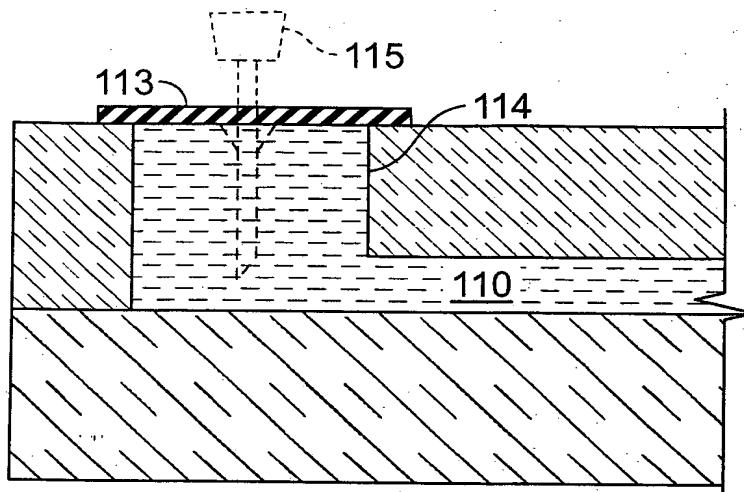
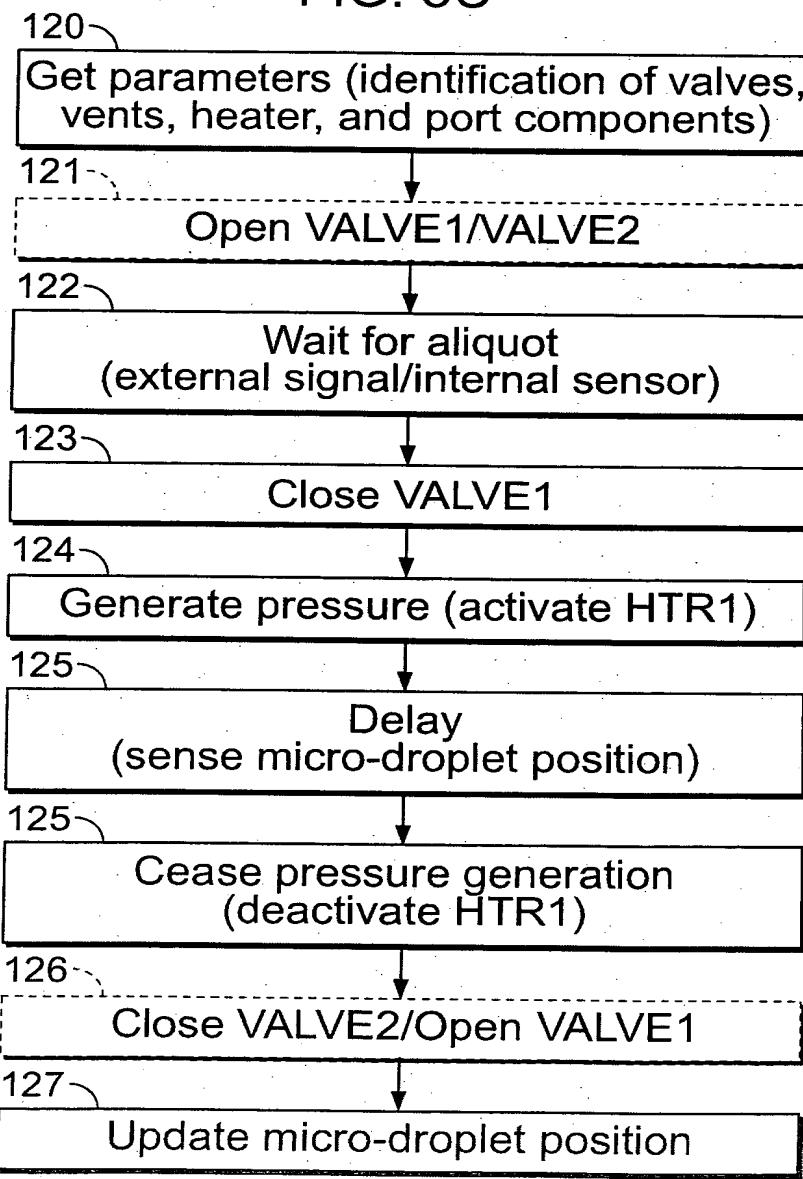


FIG. 8C



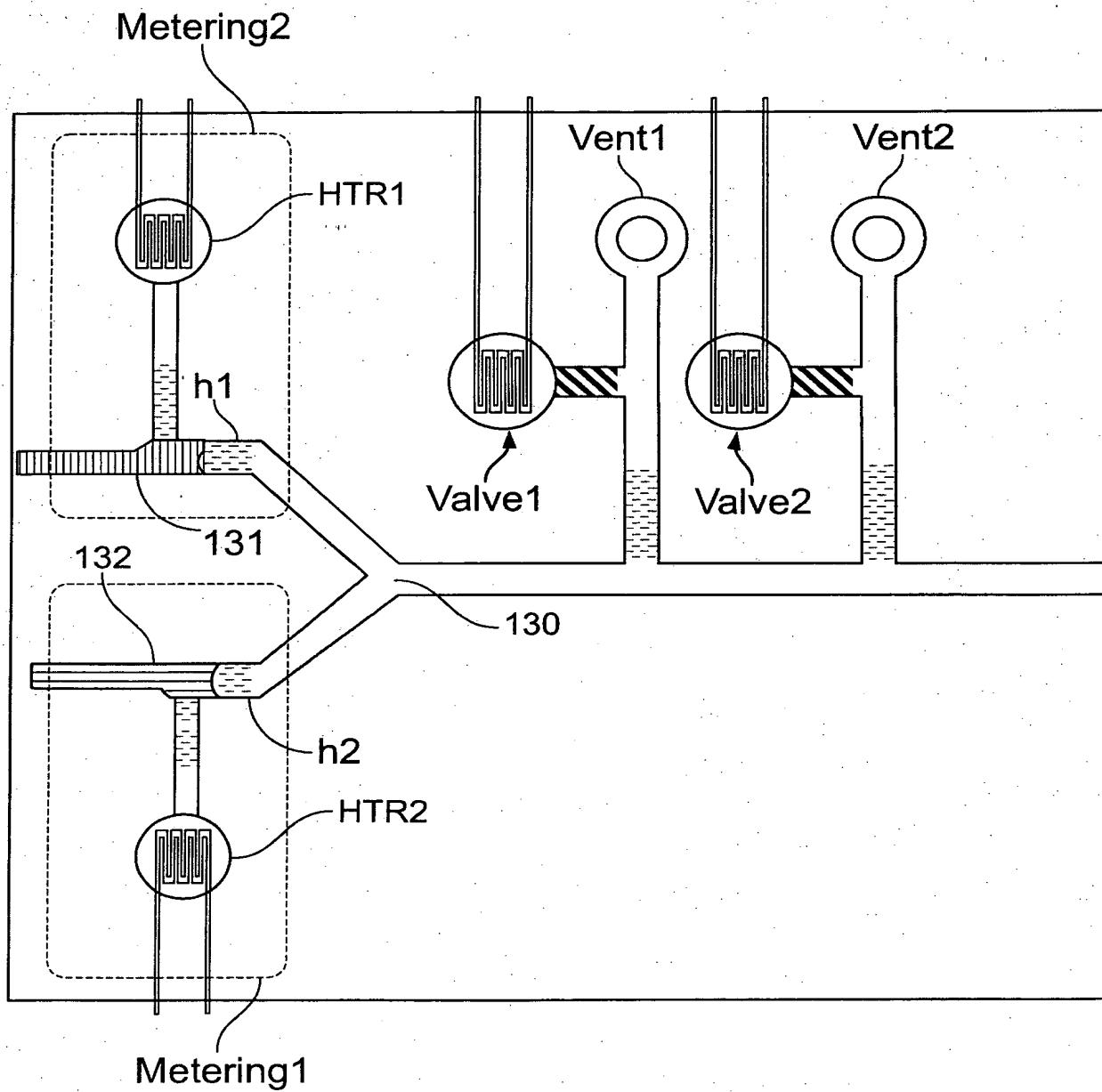


FIG. 9A

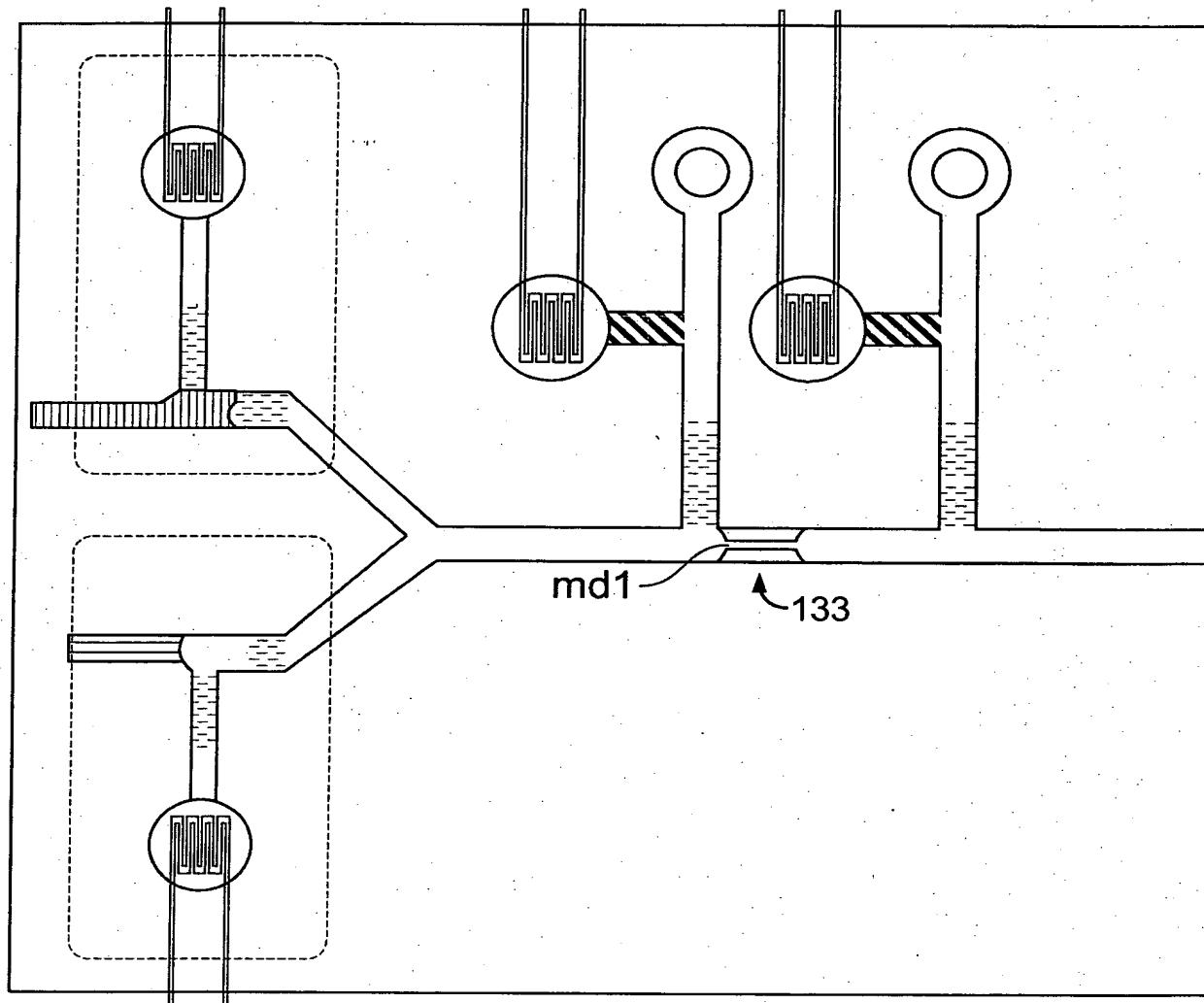


FIG. 9B

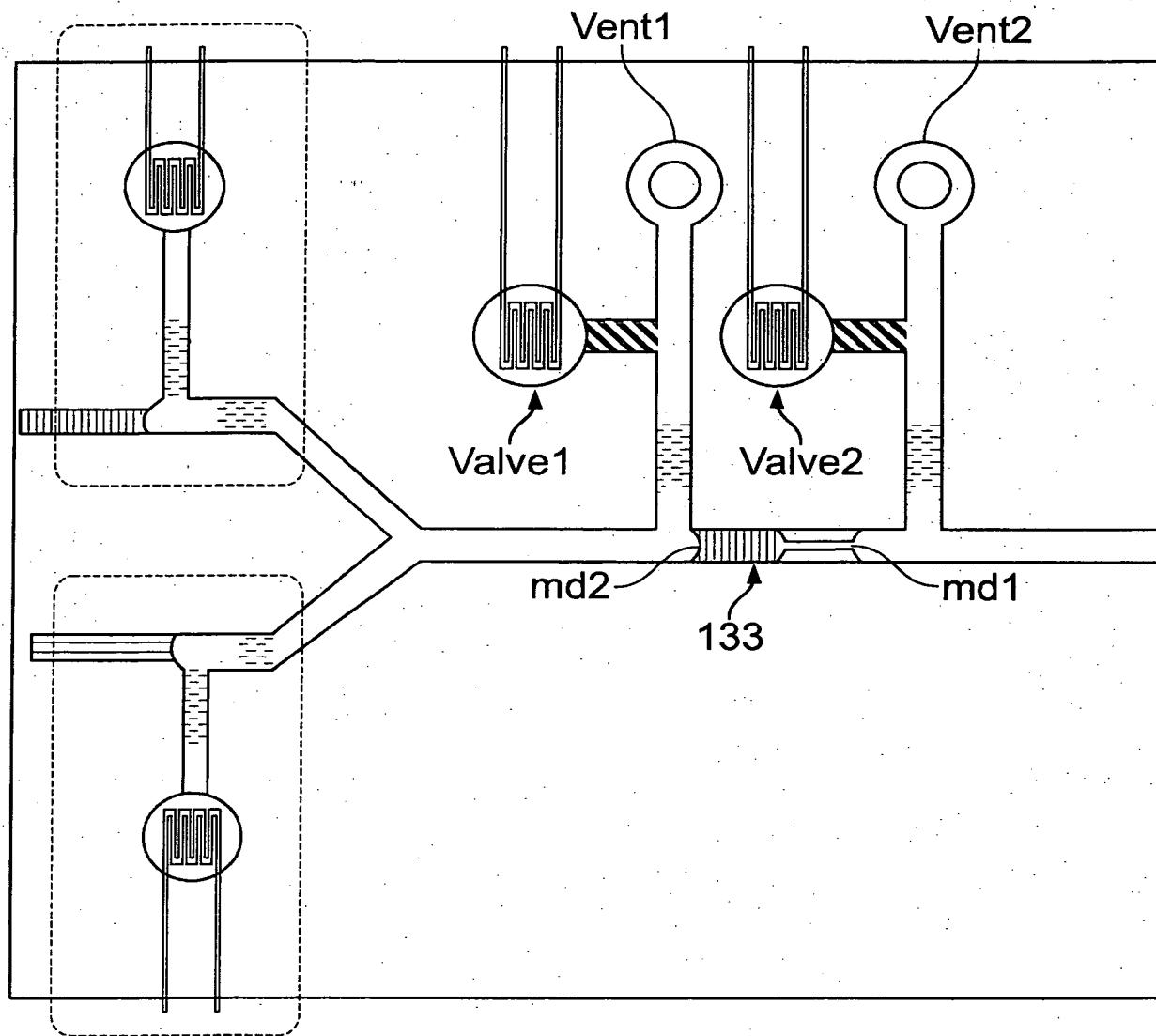
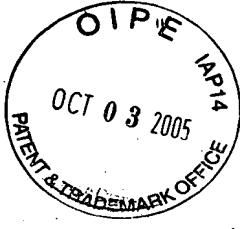


FIG. 9C

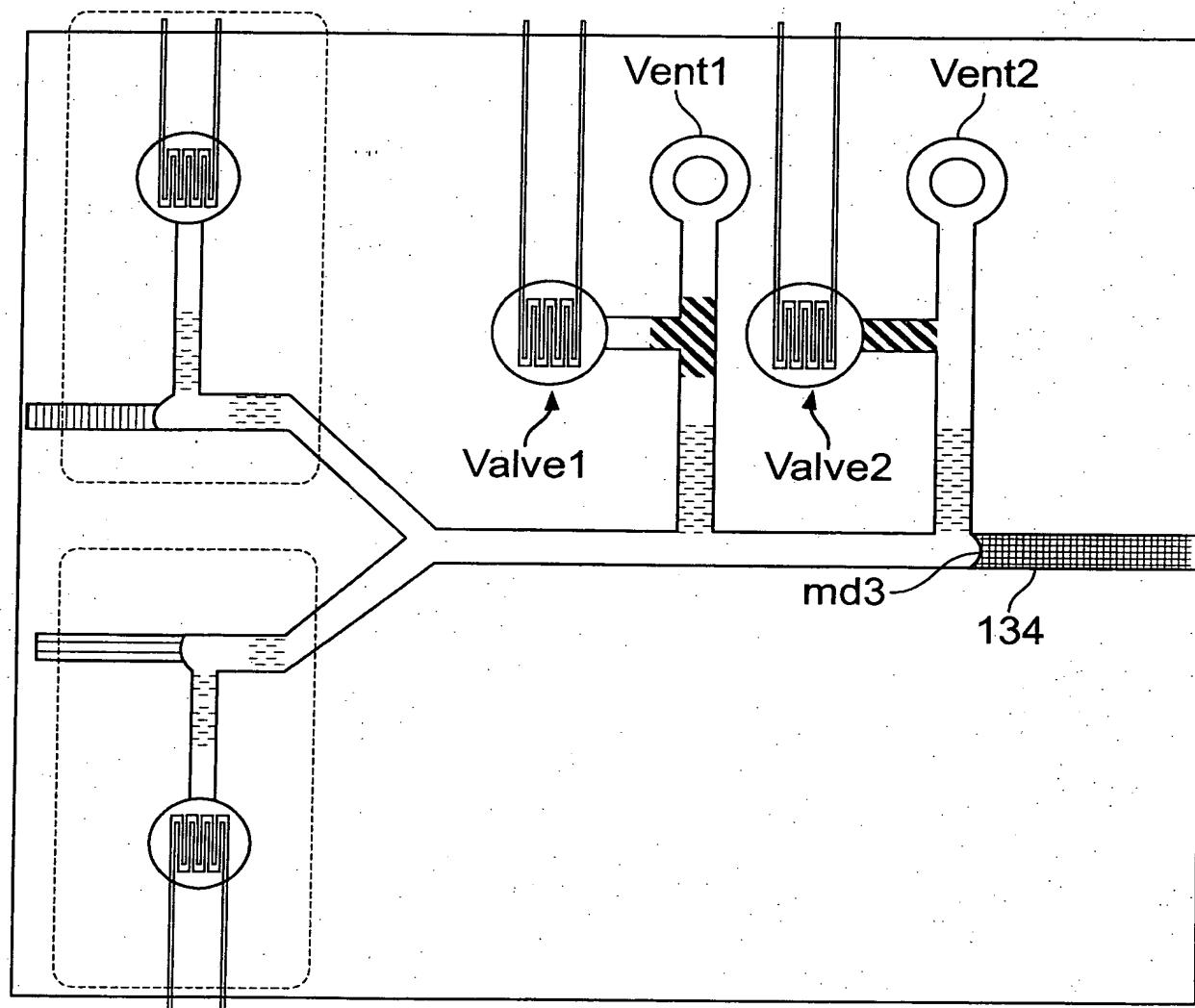


FIG. 9D

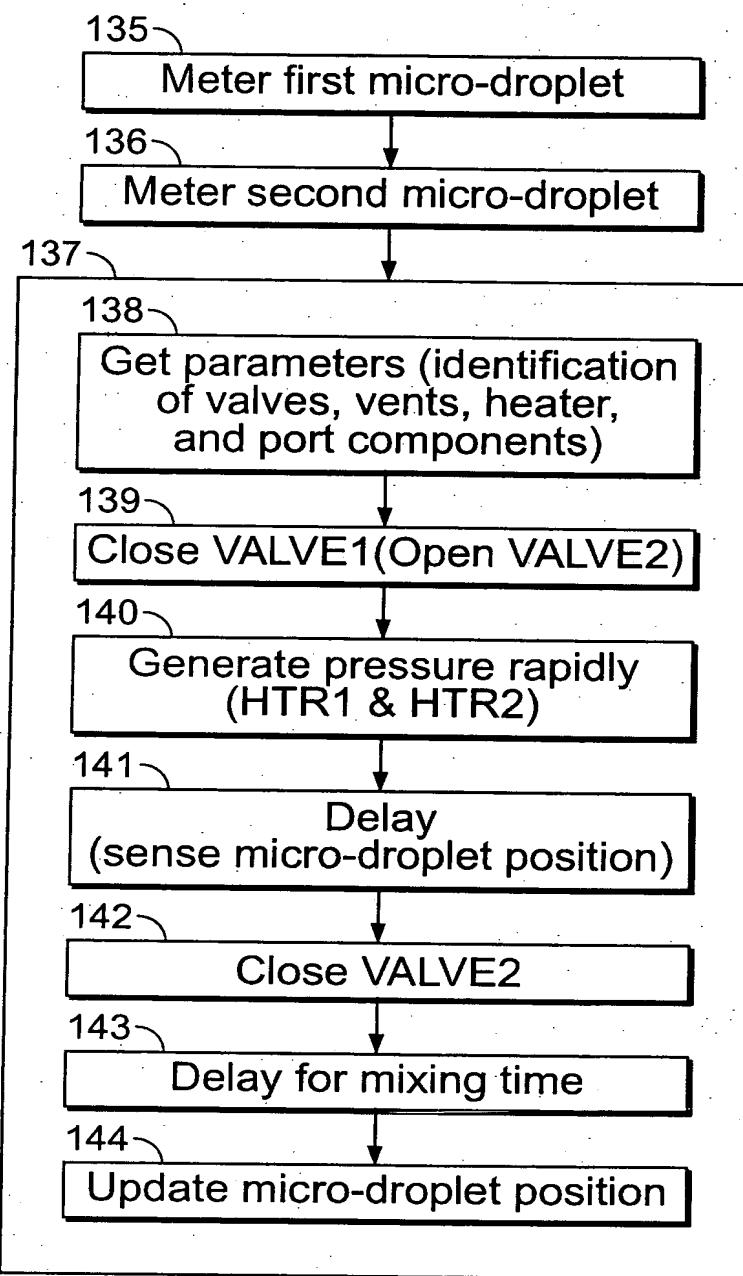
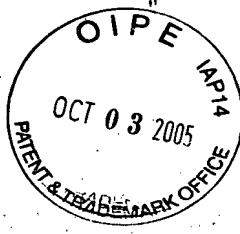


FIG. 9E

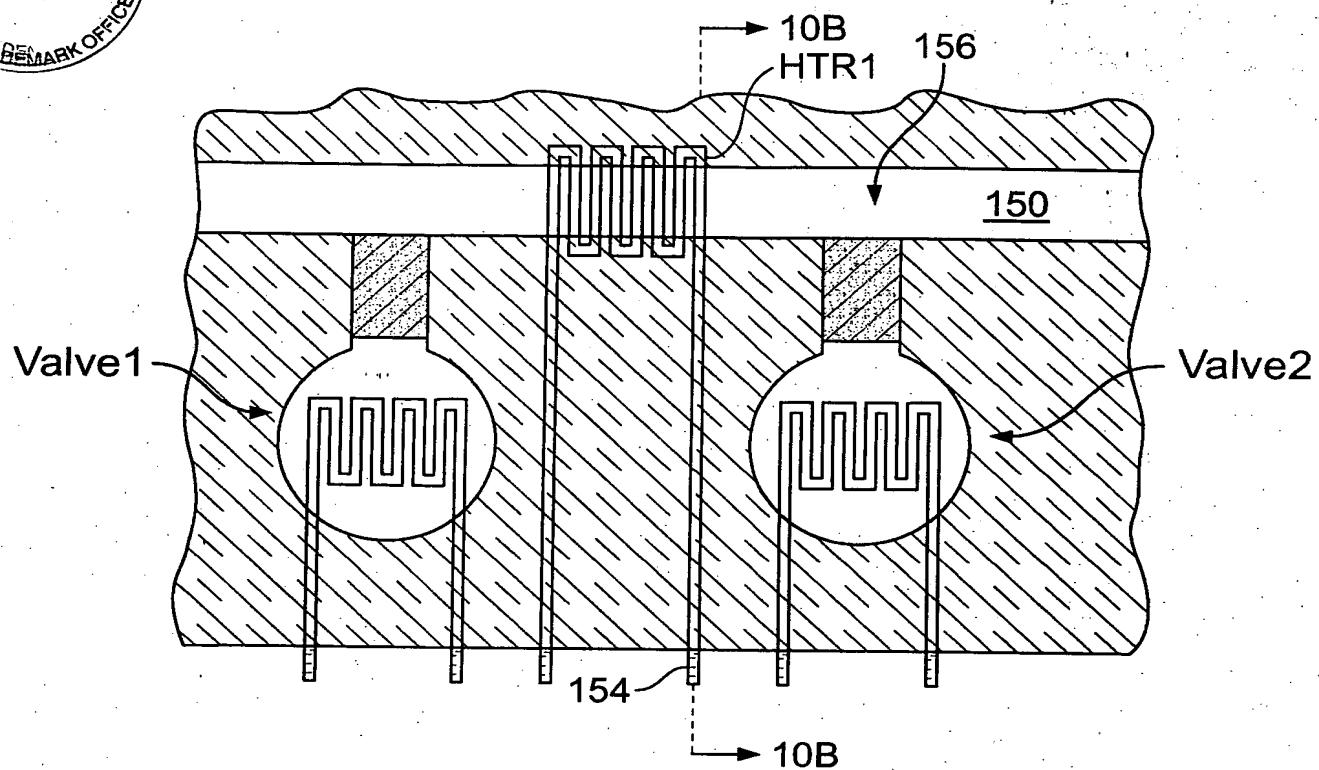
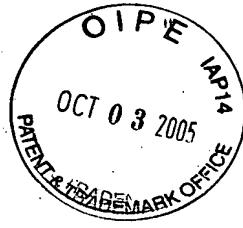


FIG. 10A

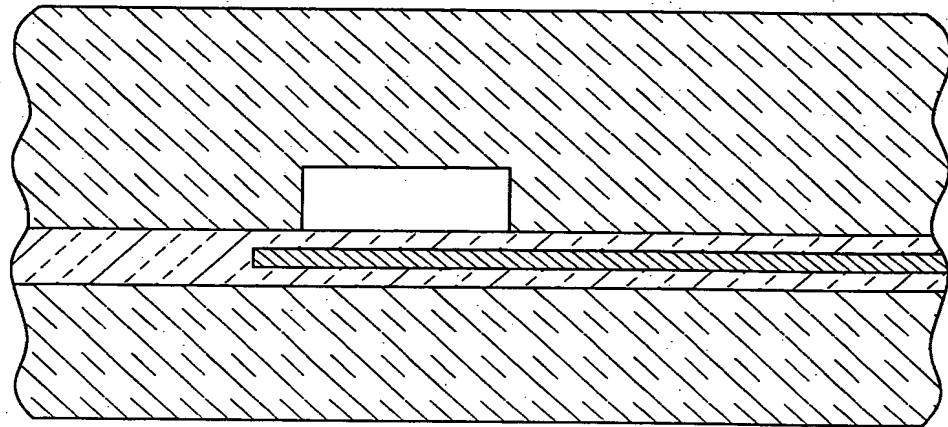


FIG. 10B

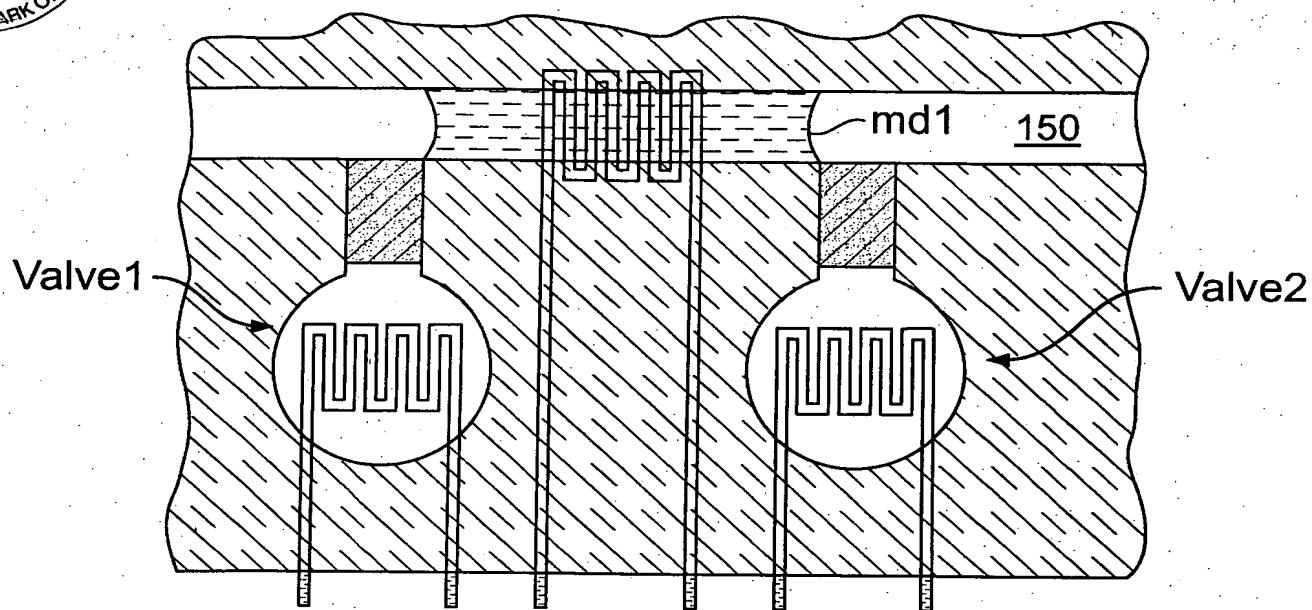
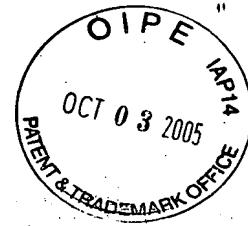


FIG. 10C

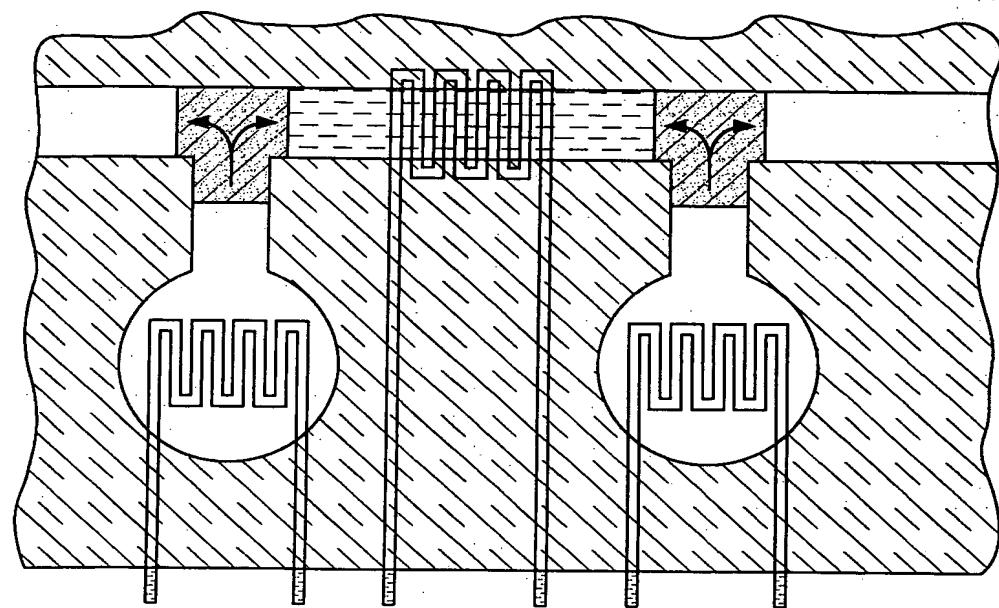


FIG. 10D

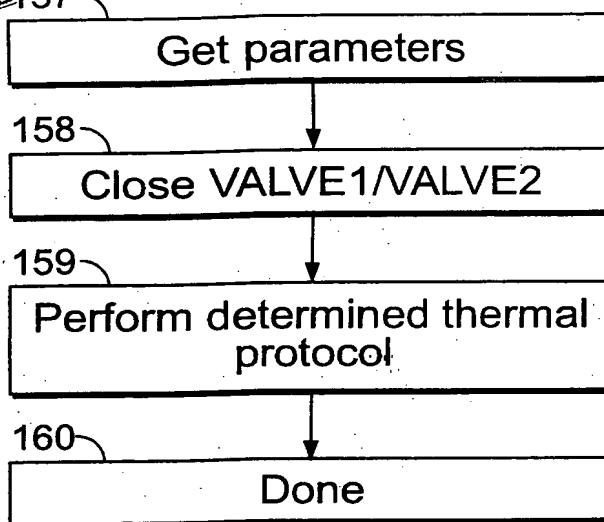


FIG. 10E

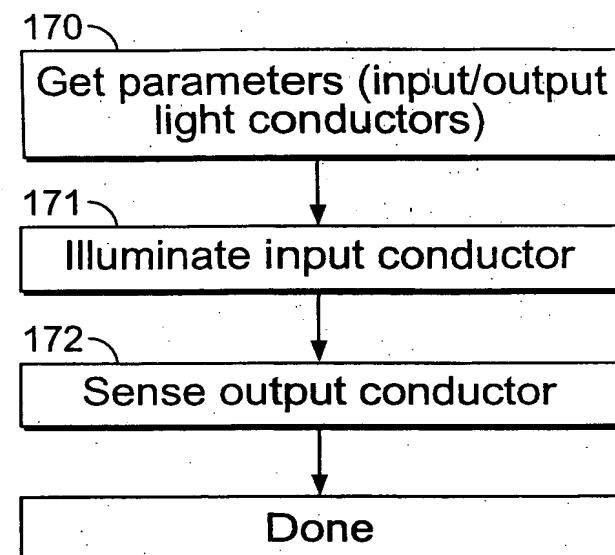


FIG. 11B

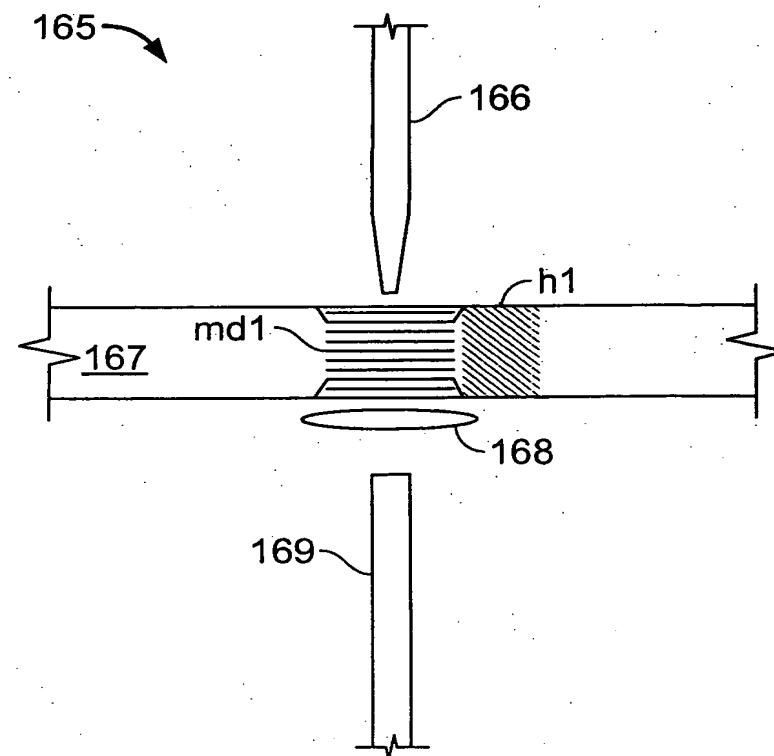


FIG. 11A

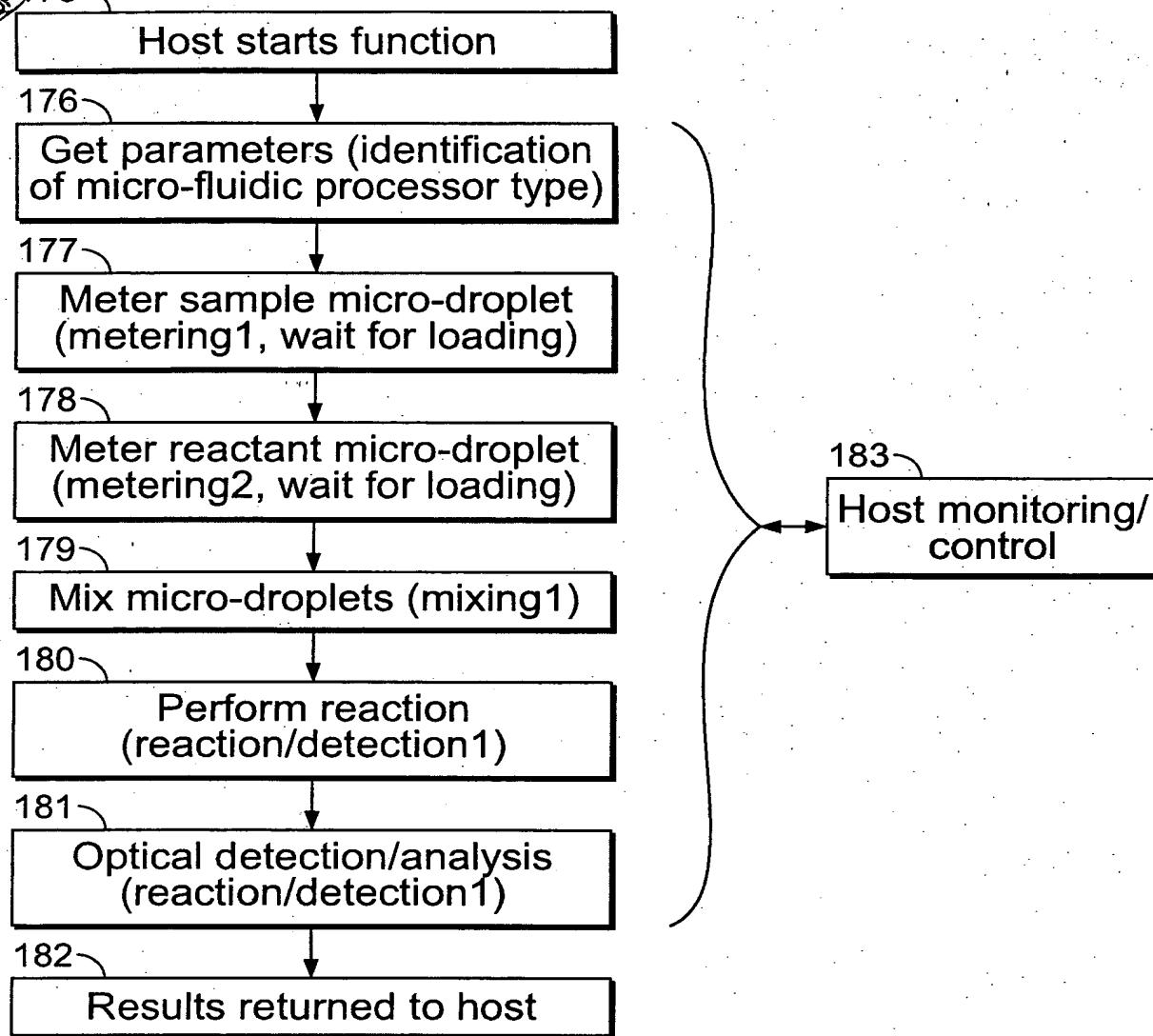
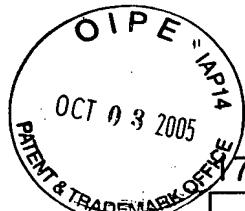


FIG. 12

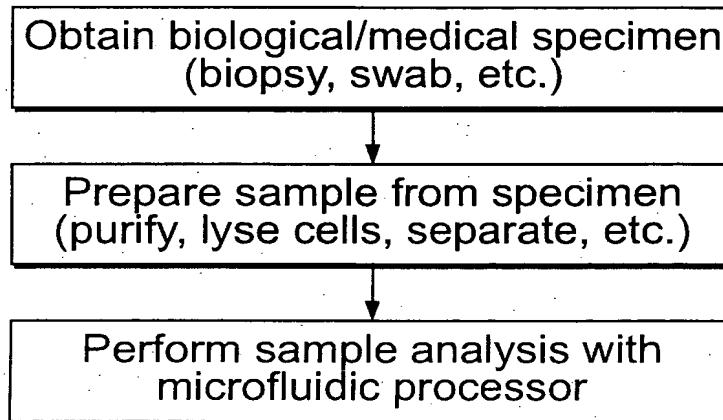


FIG. 13